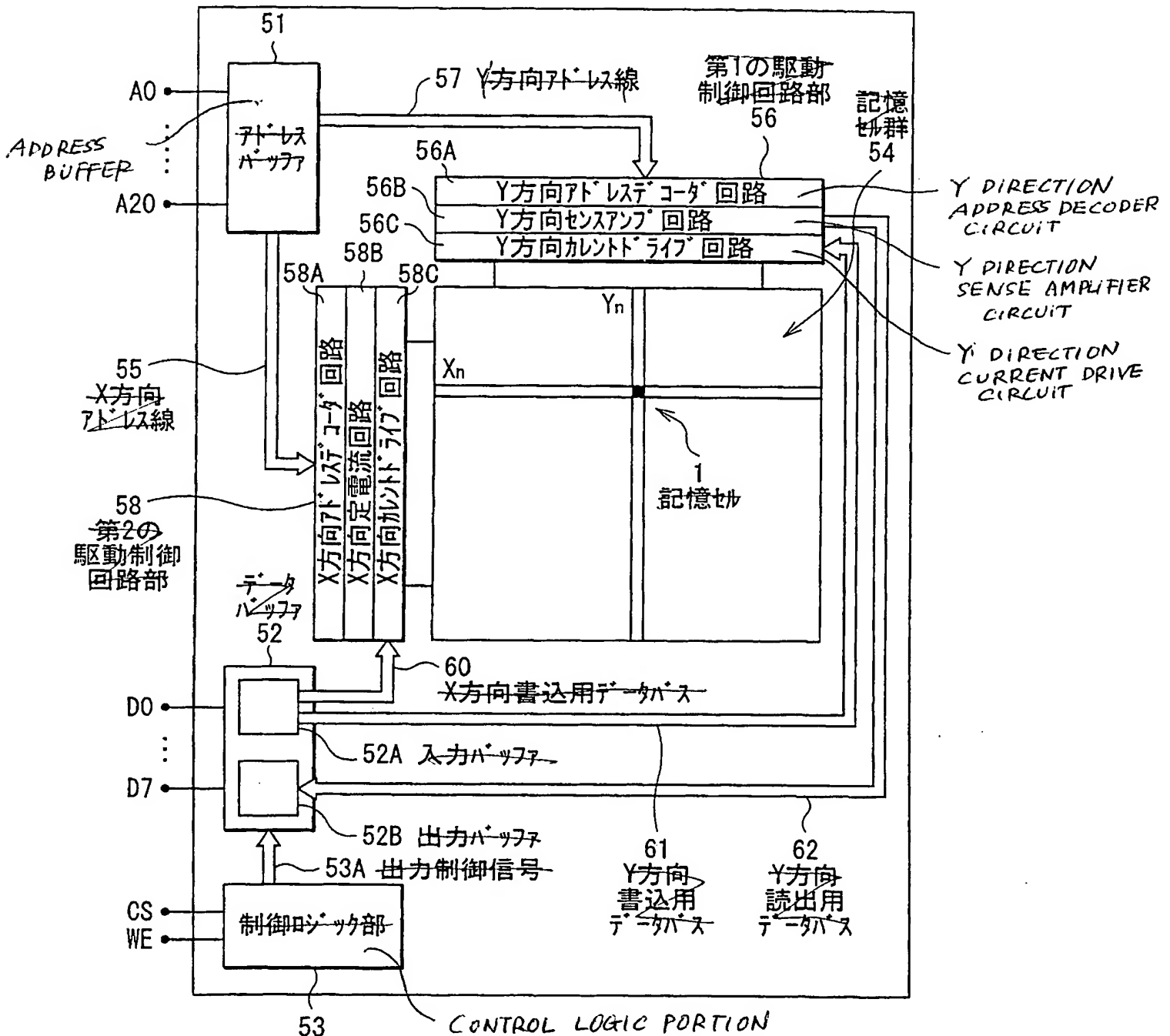


[書類名] 図面

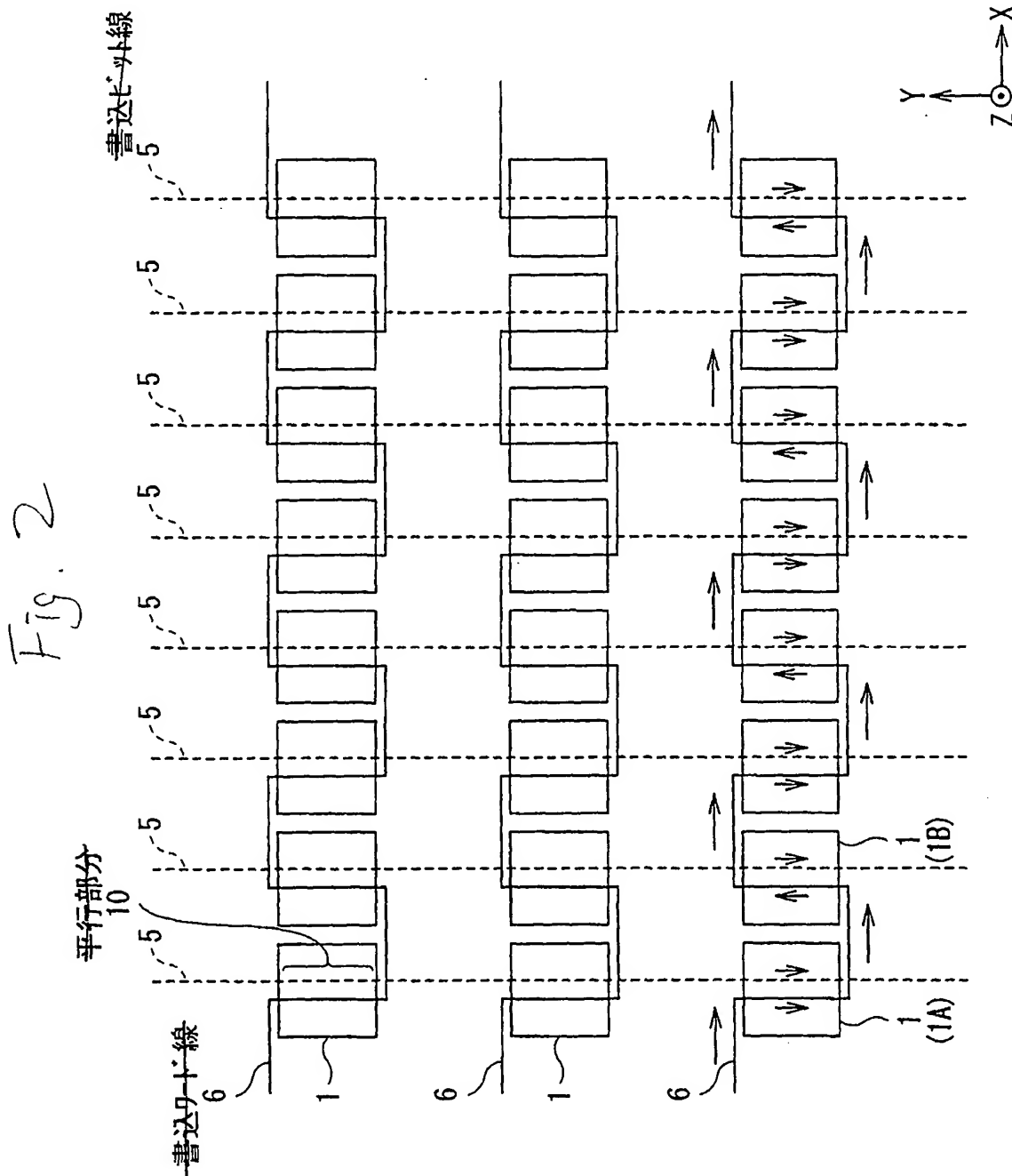
[図 1]

Fig. 1



58A. X DIRECTION ADDRESS DECODER CIRCUIT
58B. X DIRECTION CONSTANT CURRENT CIRCUIT
58C. X DIRECTION CURRENT DRIVE CIRCUIT

【図 2】



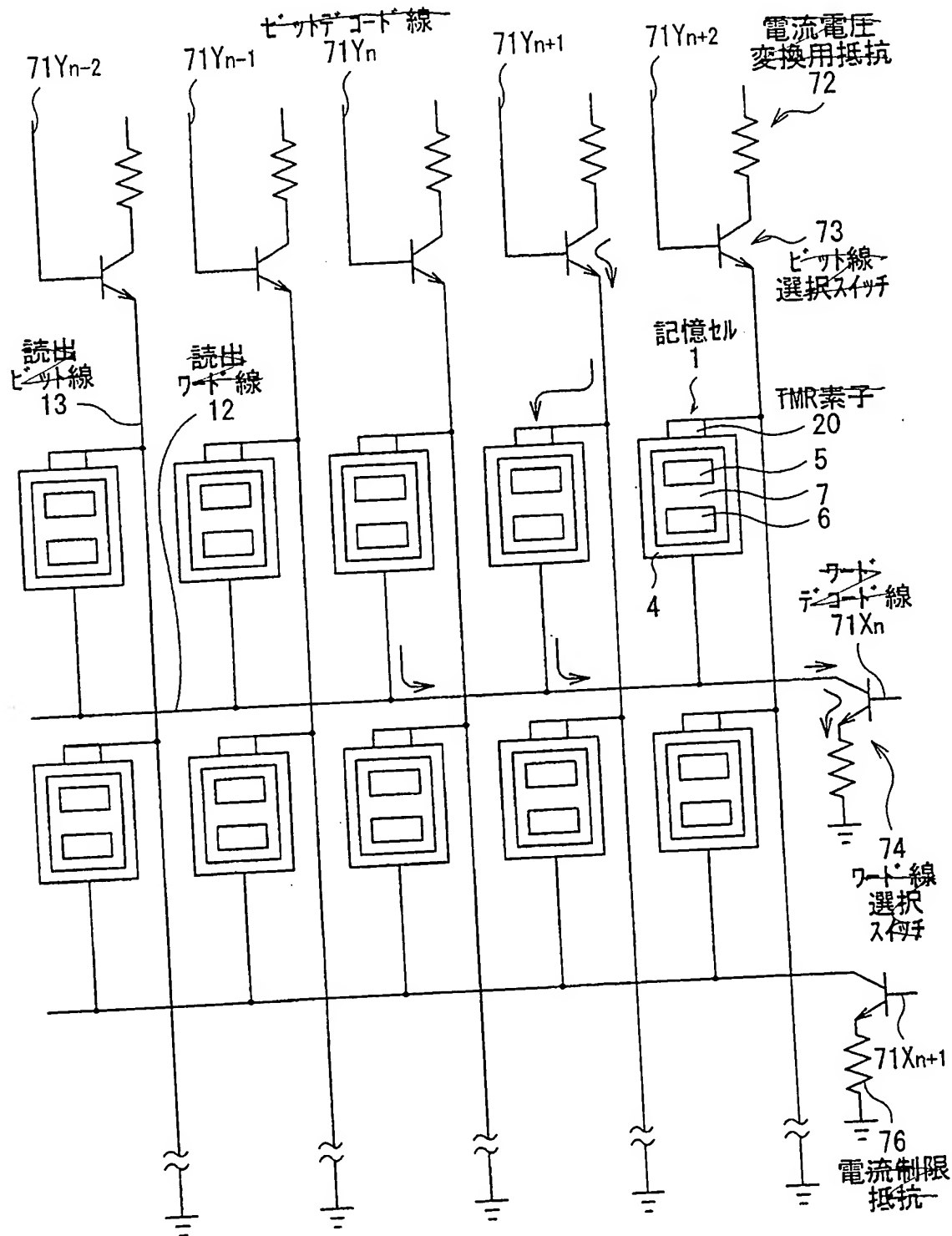
[書類名] 図面
 [特許] 2002-307686

[受付日] 2002. 09. 13

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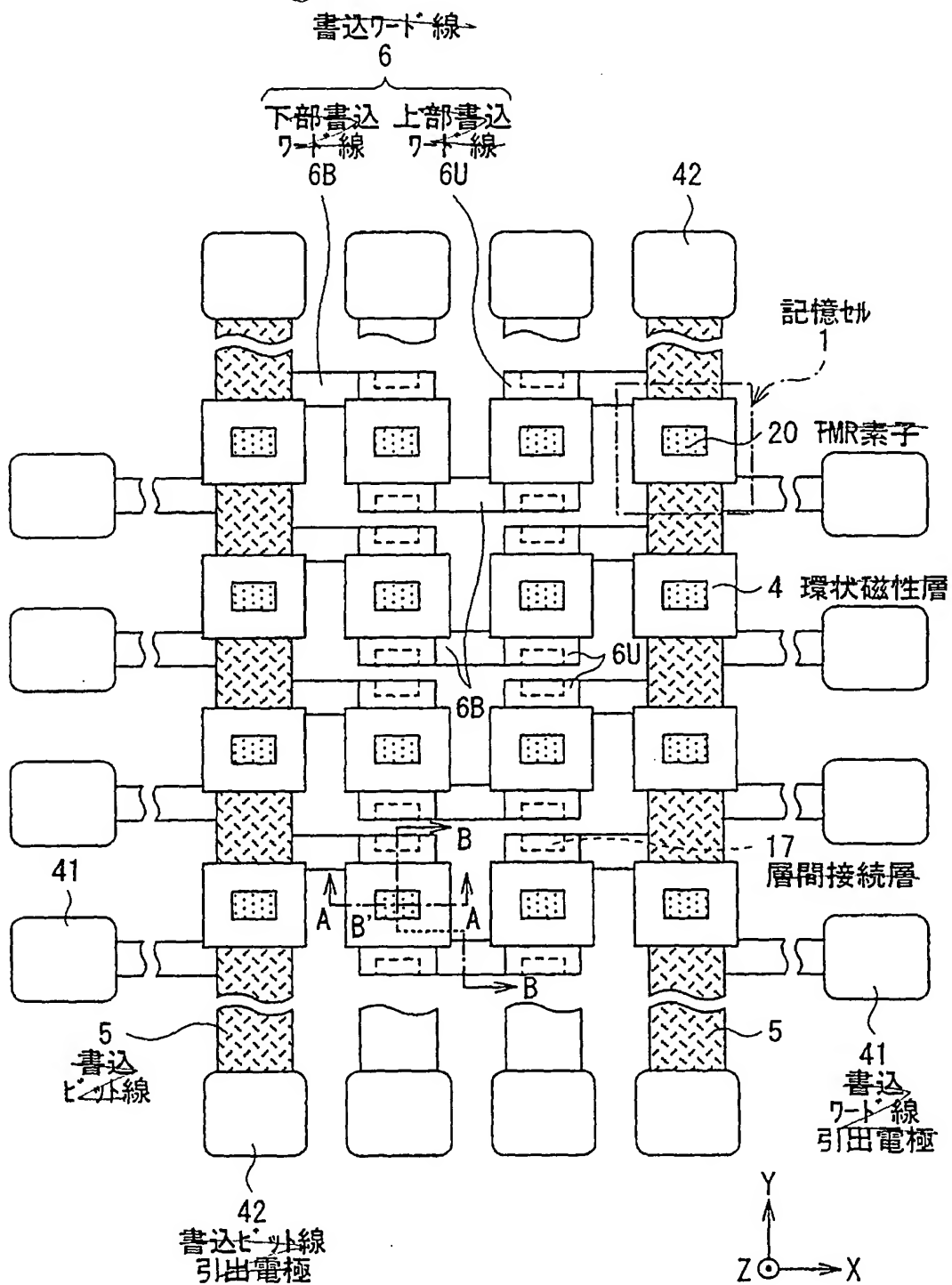
【図 3】

Fig. 3

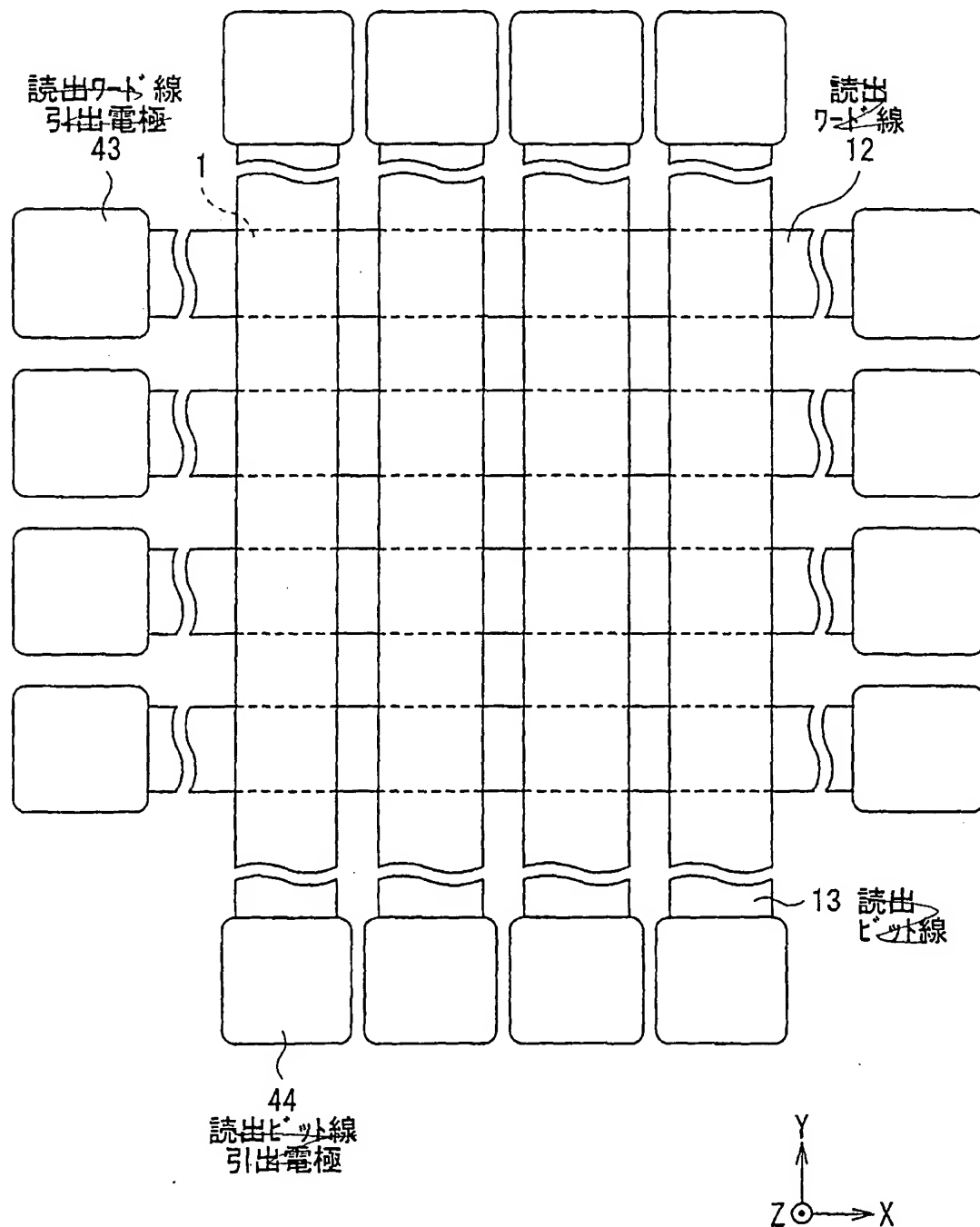


【図 4】

Fig. 4



【図5】 Fig. 5



【図 6】

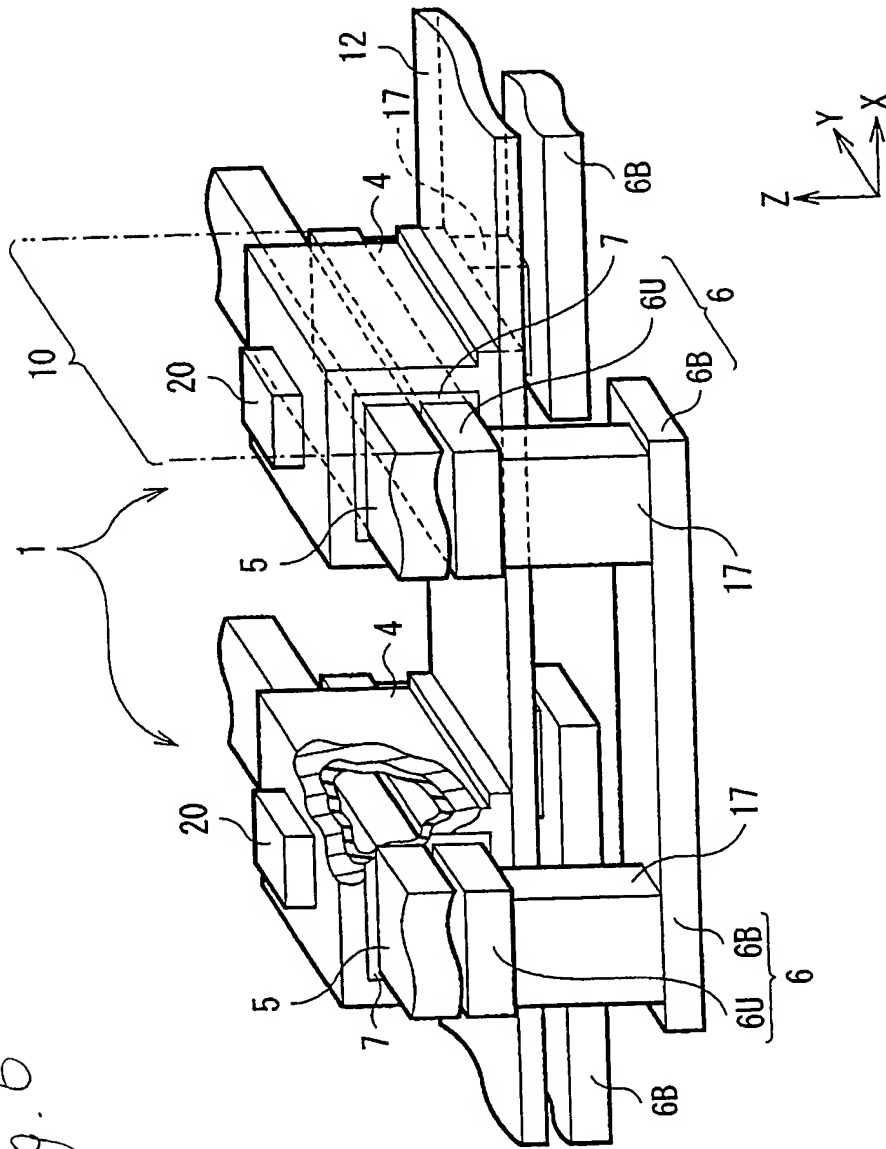
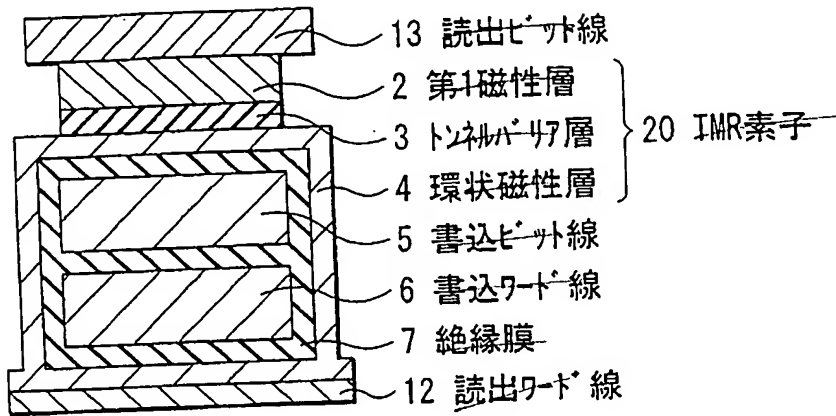


Fig. 6

【図 7】

Fig. 7A



(A)

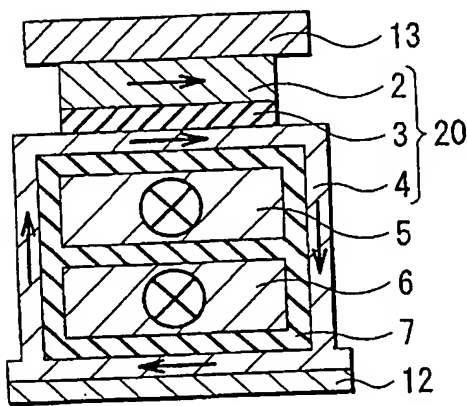
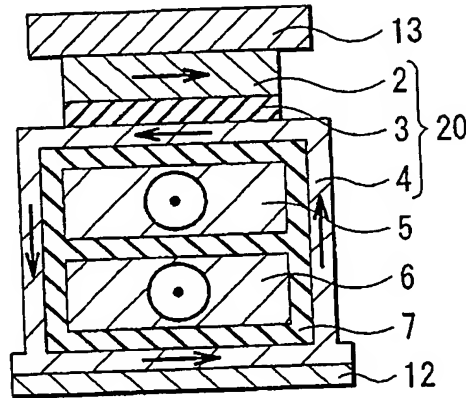
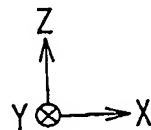


Fig. 7B (B)

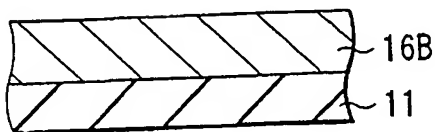


(C)

Fig. 7C

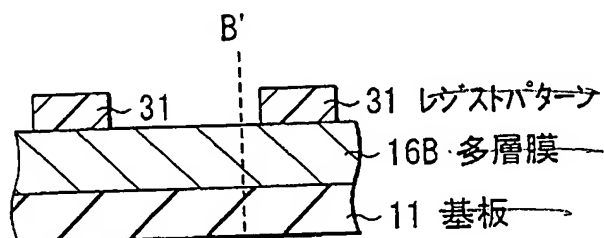


【図 8】 Fig. 8A



(A)

Fig. 8B



(B)

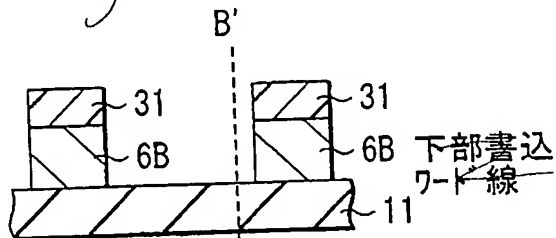
【図9】

Fig. 9A



(A)

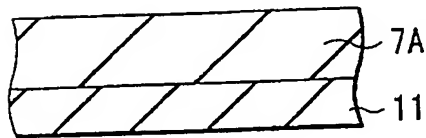
Fig. 9B



(B)

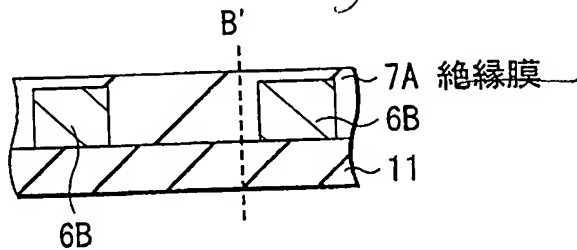
【図10】

Fig. 10A

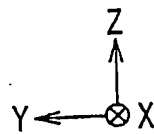
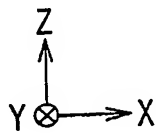


(A)

Fig. 10B

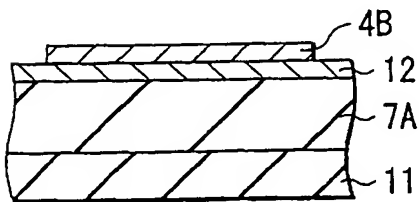


(B)



【図11】

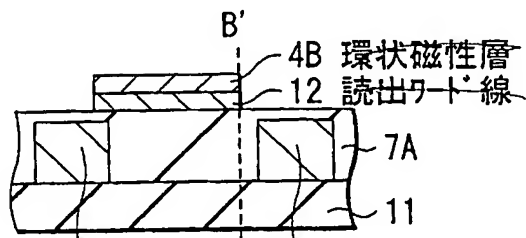
Fig. 11A



6B

(A)

Fig. 11B

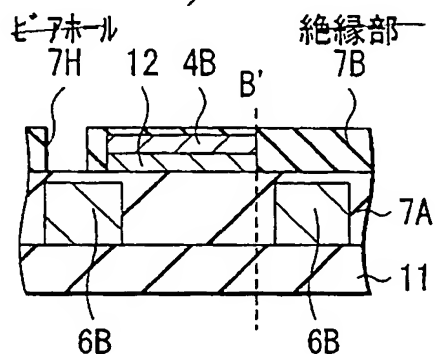


6B

6B

(B)

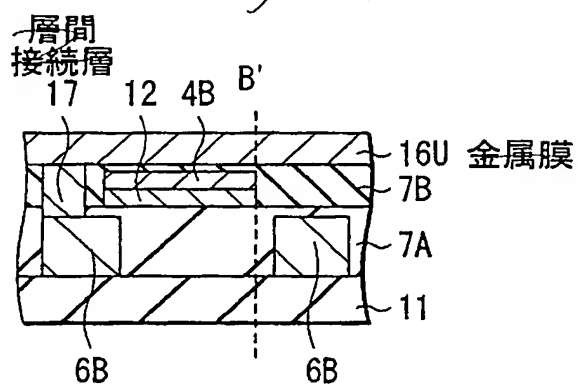
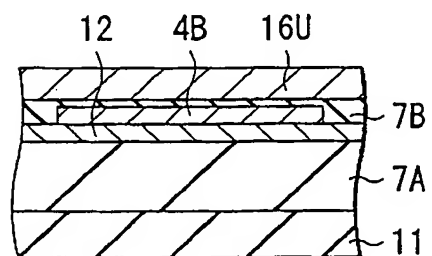
Fig. 12 B



~~(A)~~

~~(B)~~

Fig. 13B



~~(A)~~

~~(B)~~

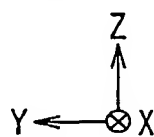
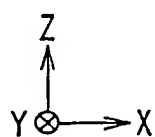
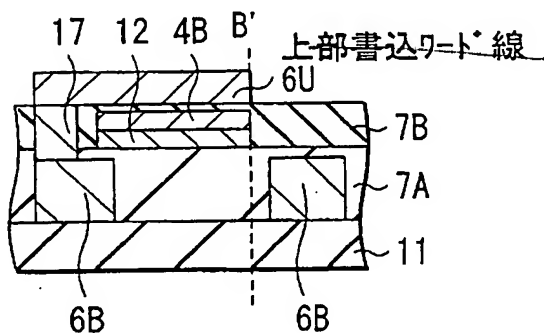
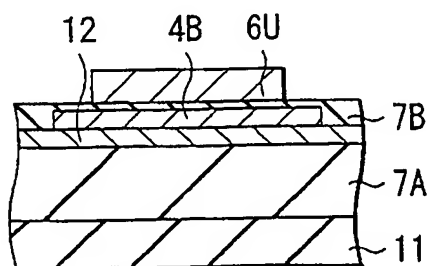


Fig. 14B



~~(A)~~

(B) _____

【図15】 Fig. 15A

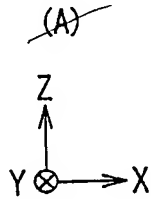
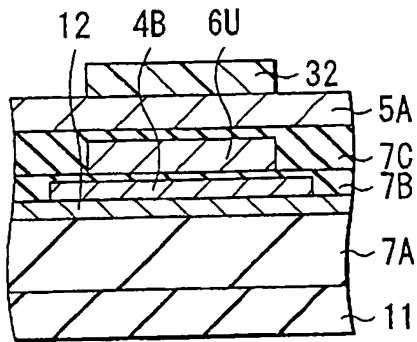
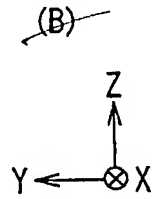
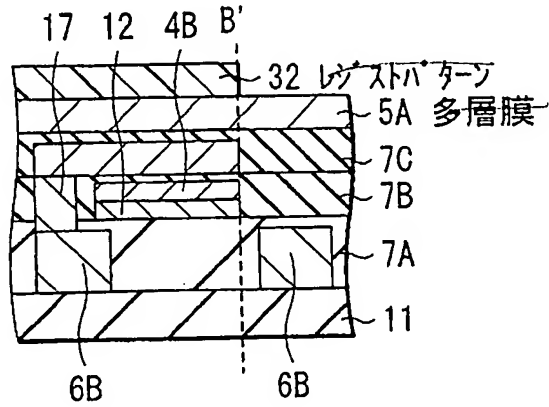
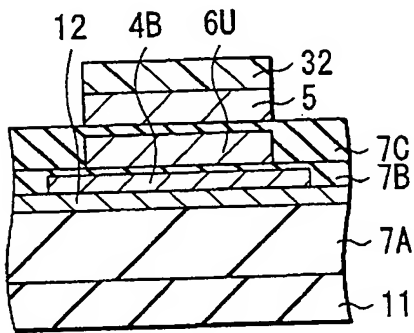


Fig. 15B

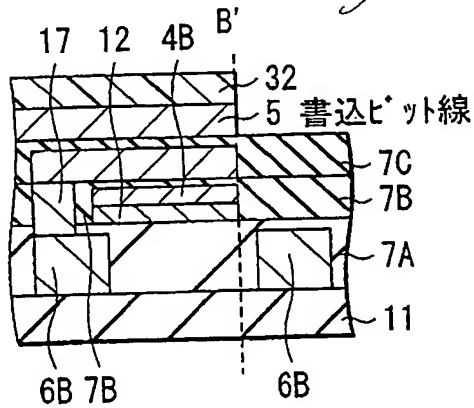


【図16】 Fig. 16A



(A)

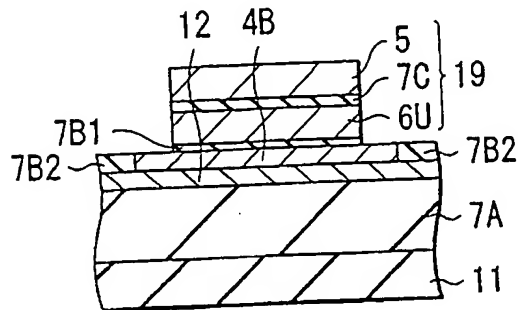
Fig. 16B



(B)

【図17】

Fig. 17A



(A)

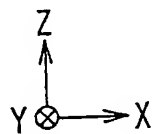
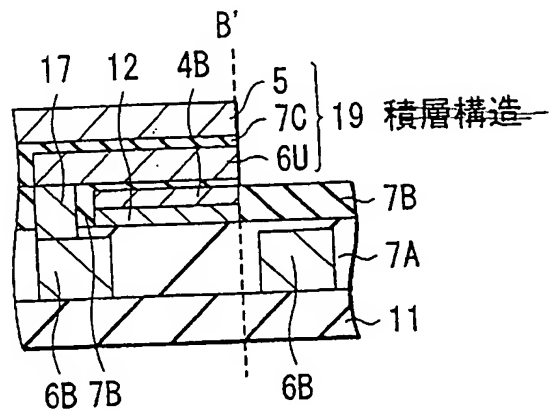
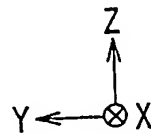


Fig. 17B

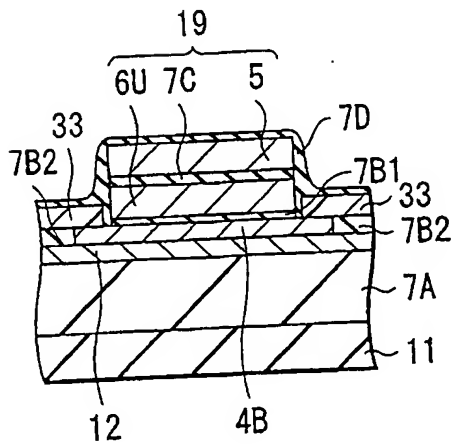


(B)



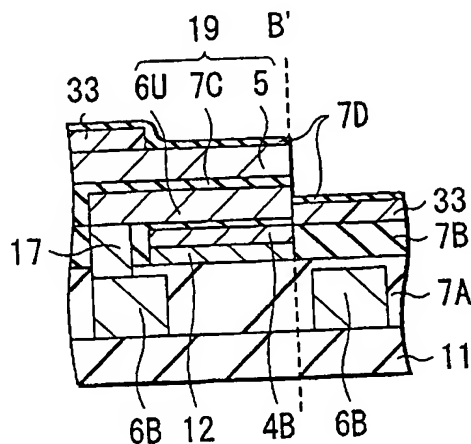
【図18】

Fig. 18A



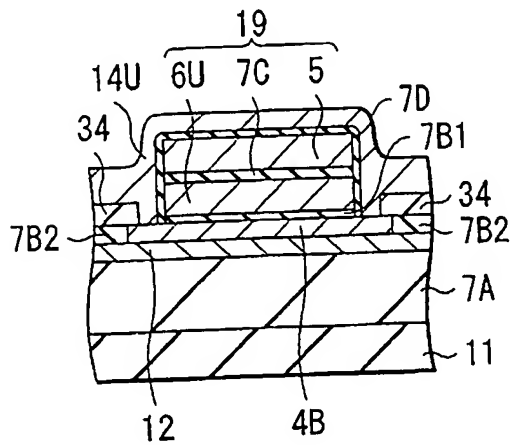
(A)

Fig. 18B



(B)

【図19】 Fig. 19A



(A)

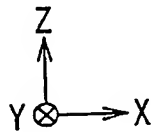
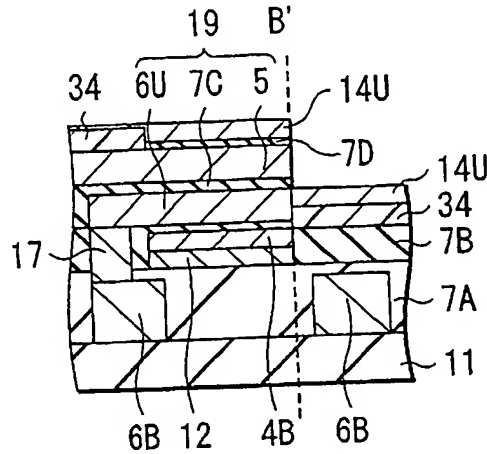
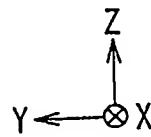


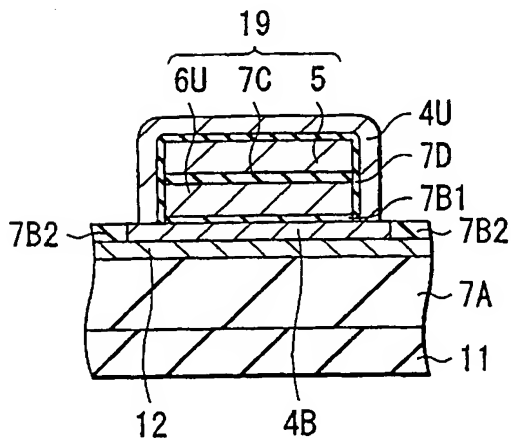
Fig. 19B



(B)

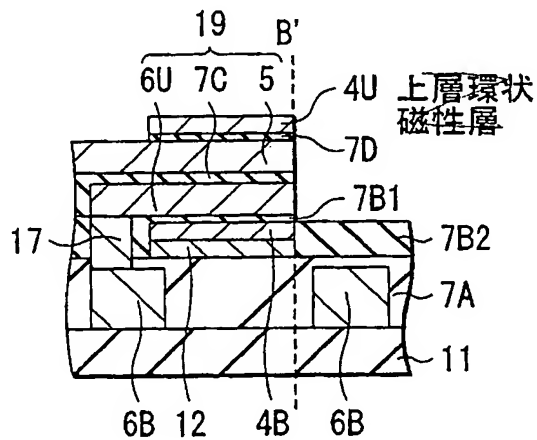


【図20】 Fig. 20A



(A)

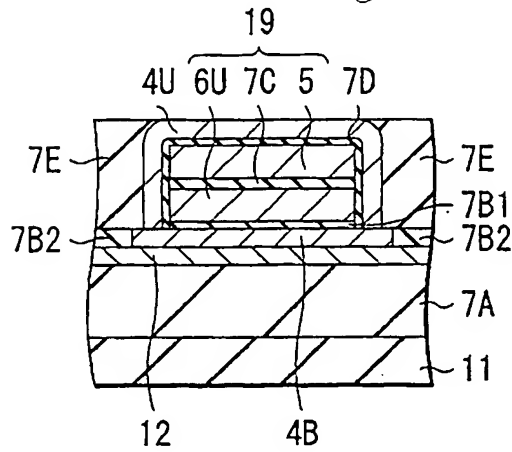
Fig. 20B



(B)

【図21】

Fig. 21A



(A)

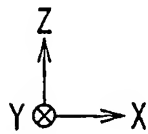
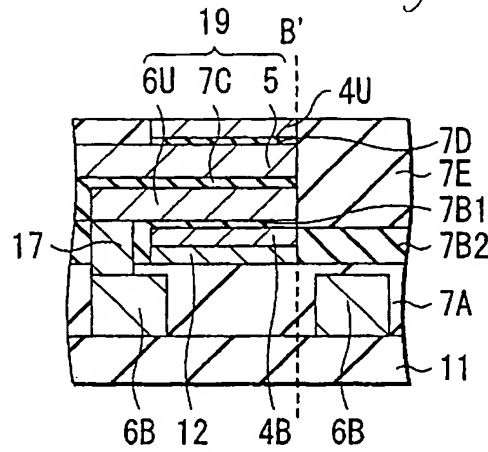
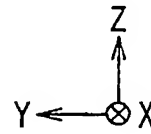


Fig. 21B

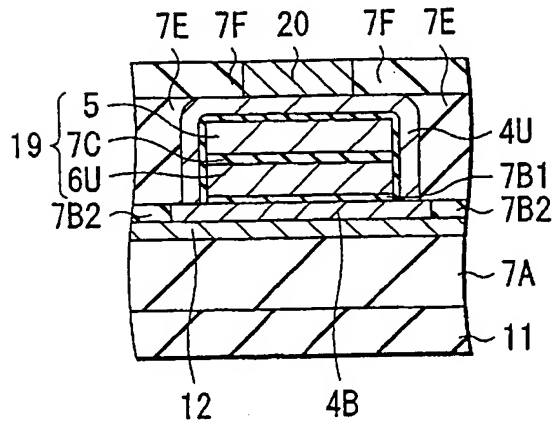


(B)



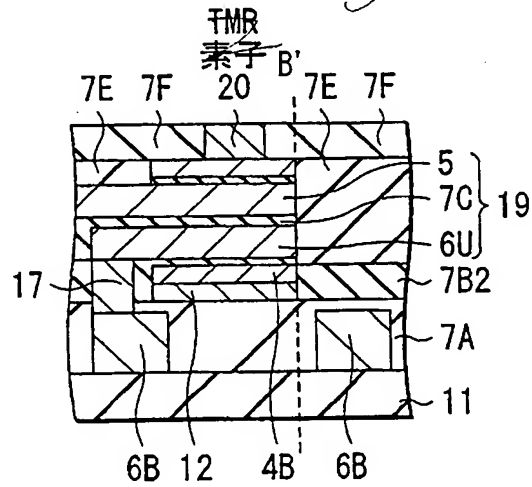
【図22】

Fig. 22A



(A)

Fig. 22B



(B)

TMR
素子

Fig. 23A

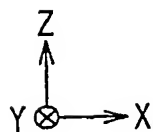
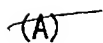
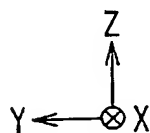
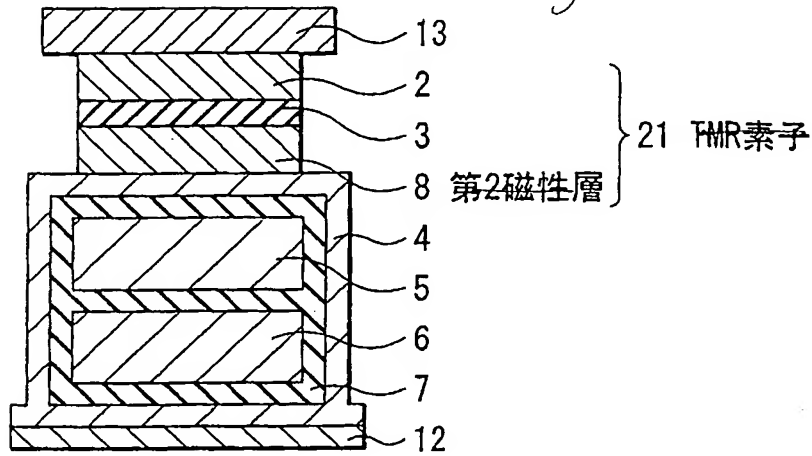


Fig. 23 B

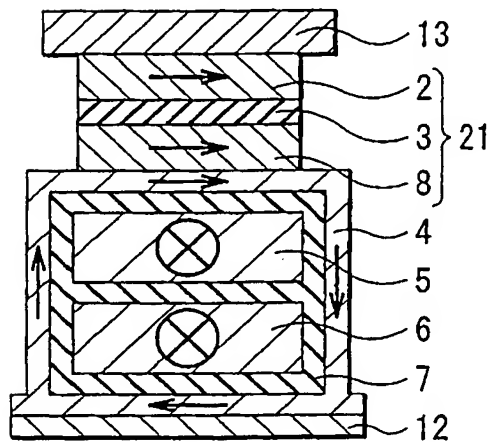


【図24】

Fig. 24A

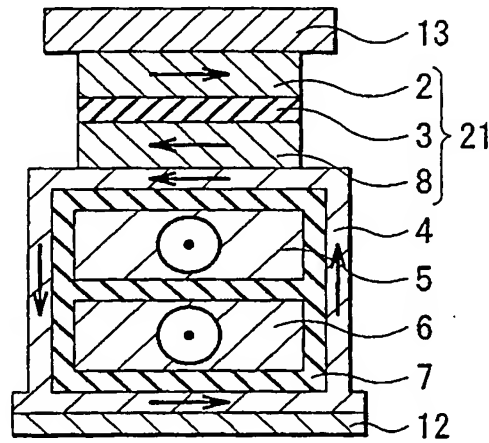


(A)



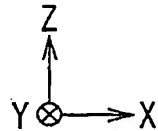
(B)

Fig. 24B

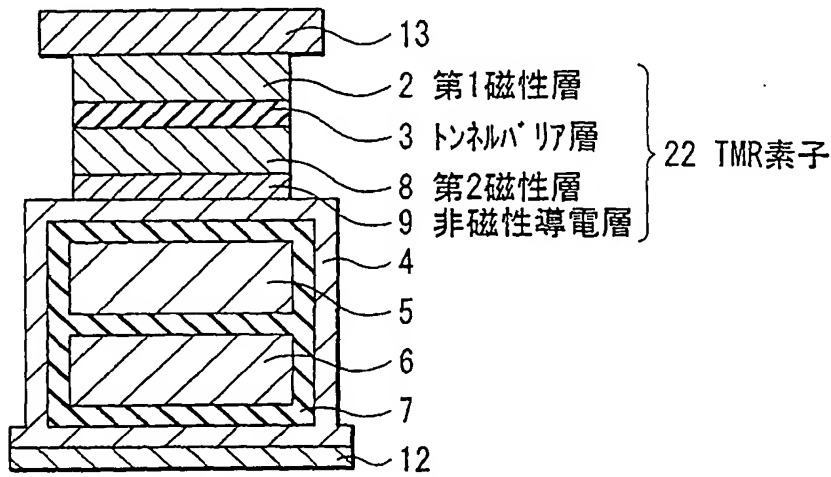


(C)

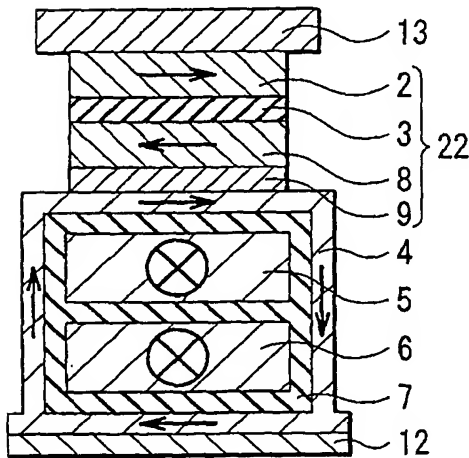
Fig. 24C



【図 25】 Fig. 25A

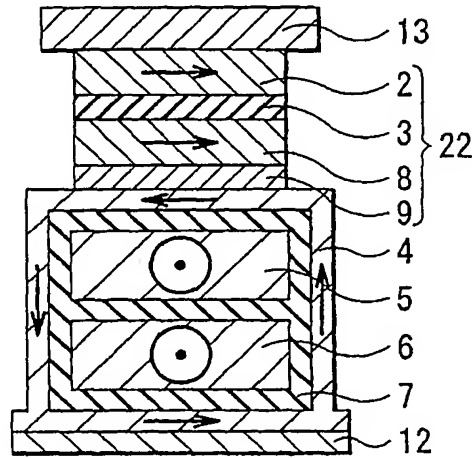


(A)



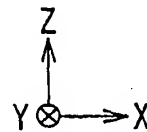
(B)

Fig. 25B



(C)

Fig. 25C



2 FIRST MAGNETIC LAYER, 3 TUNNEL BARRIER LAYER, 8 SECOND MAGNETIC LAYER, 9
NONMAGNETIC CONDUCTIVE LAYER, 22 TMR ELEMENT

【図 26】

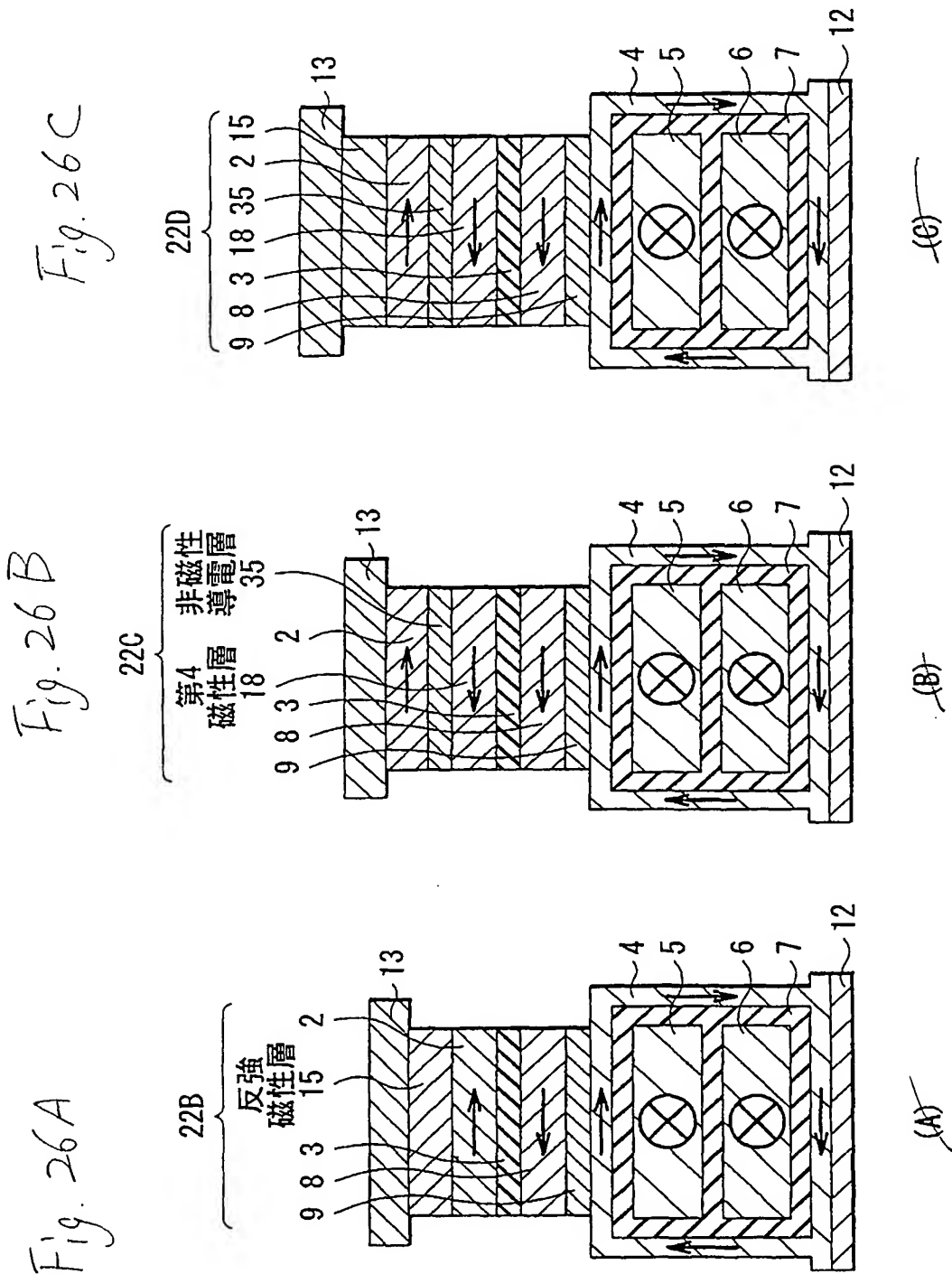


FIG 26

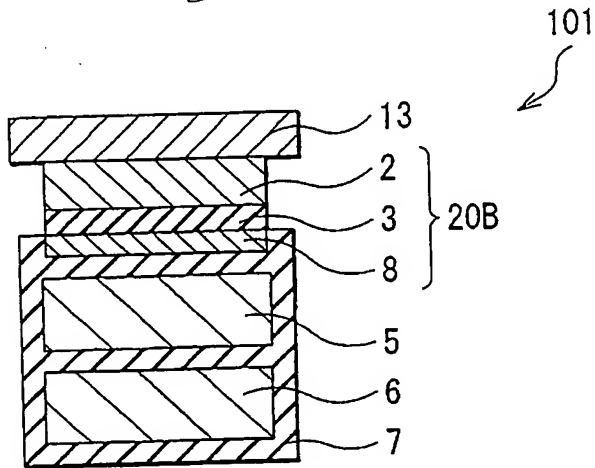
15 ANTIFERROMAGNETIC LAYER

18 FOURTH MAGNETIC LAYER

35 NONMAGNETIC CONDUCTIVE LAYER

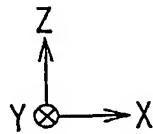
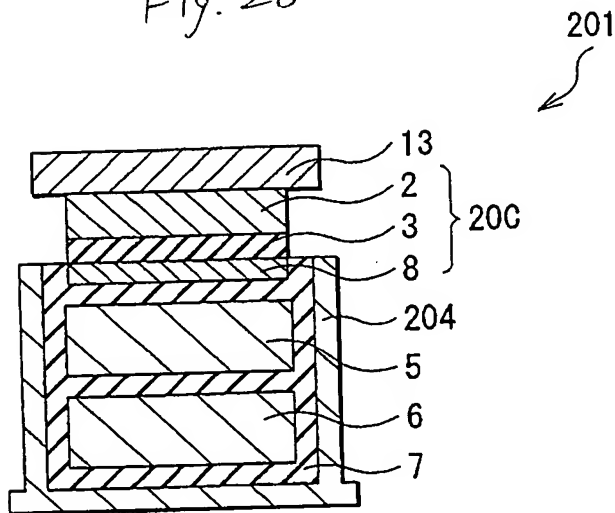
【図27】

Fig. 27

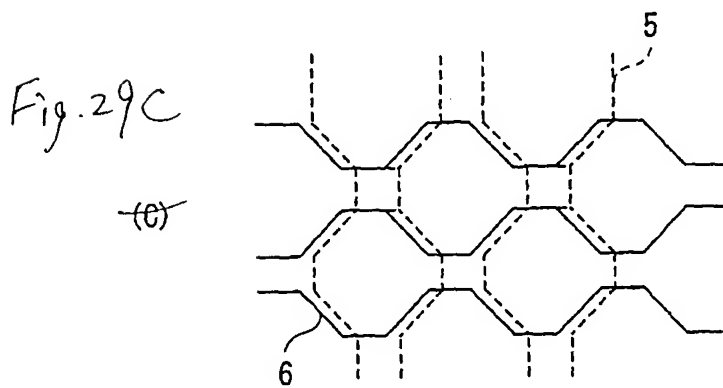
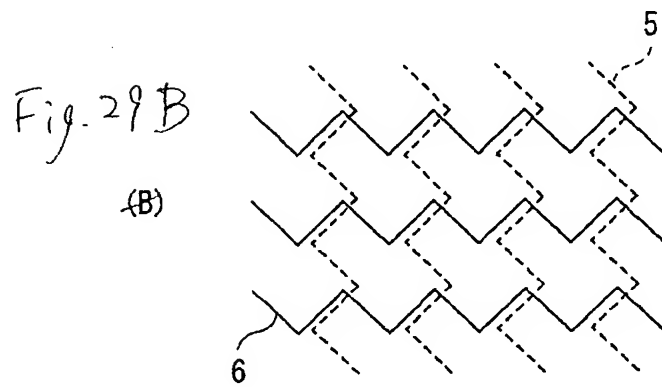
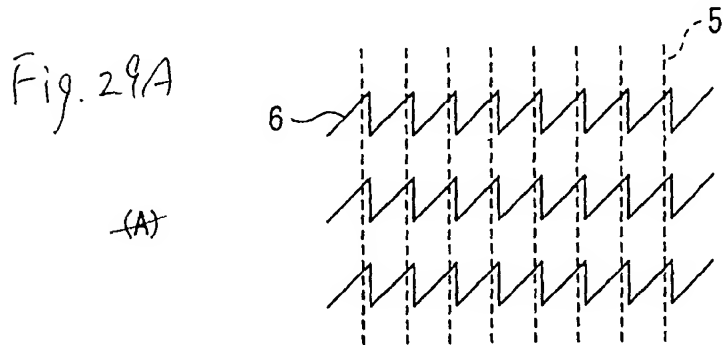


【図28】

Fig. 28

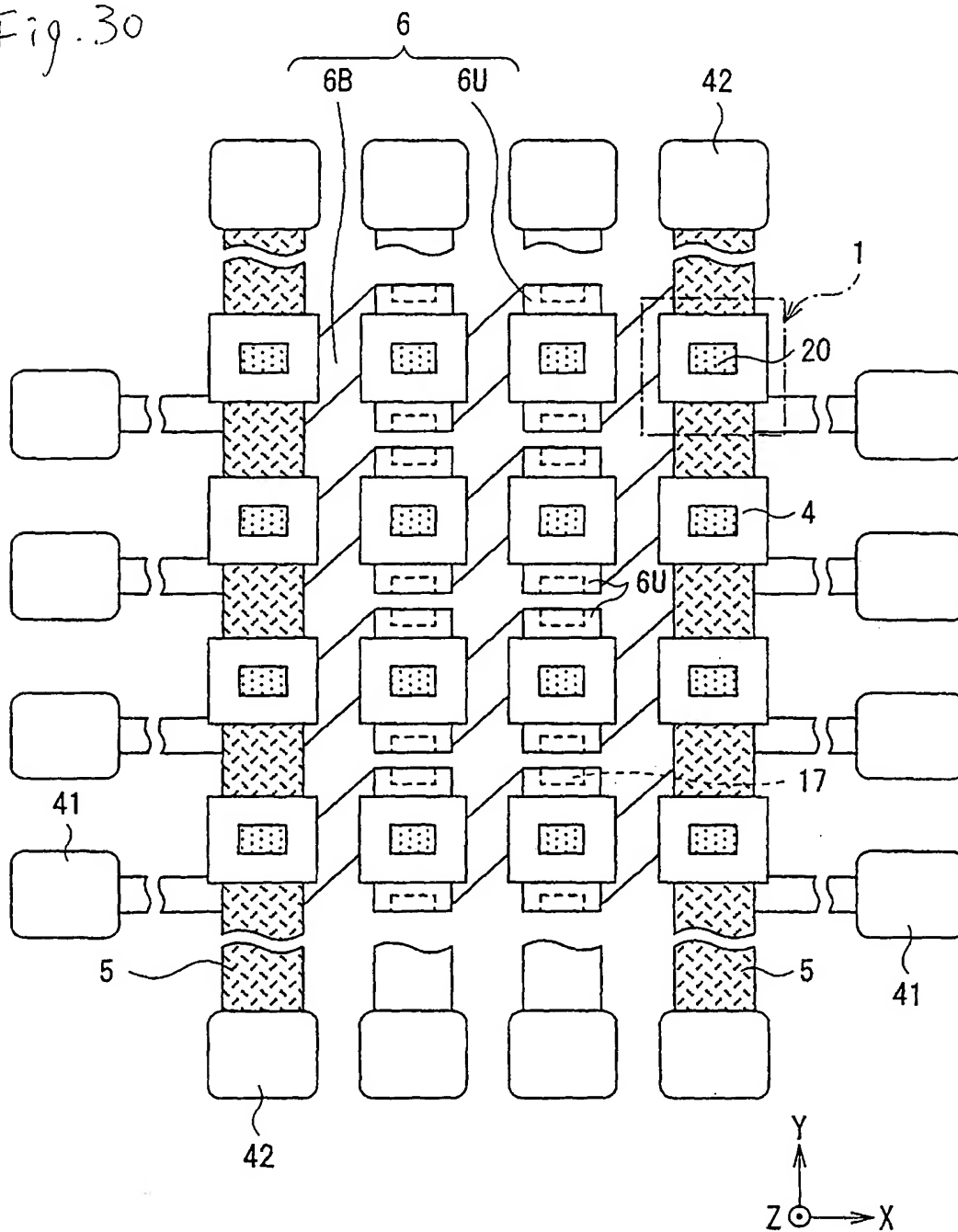


【図 29】

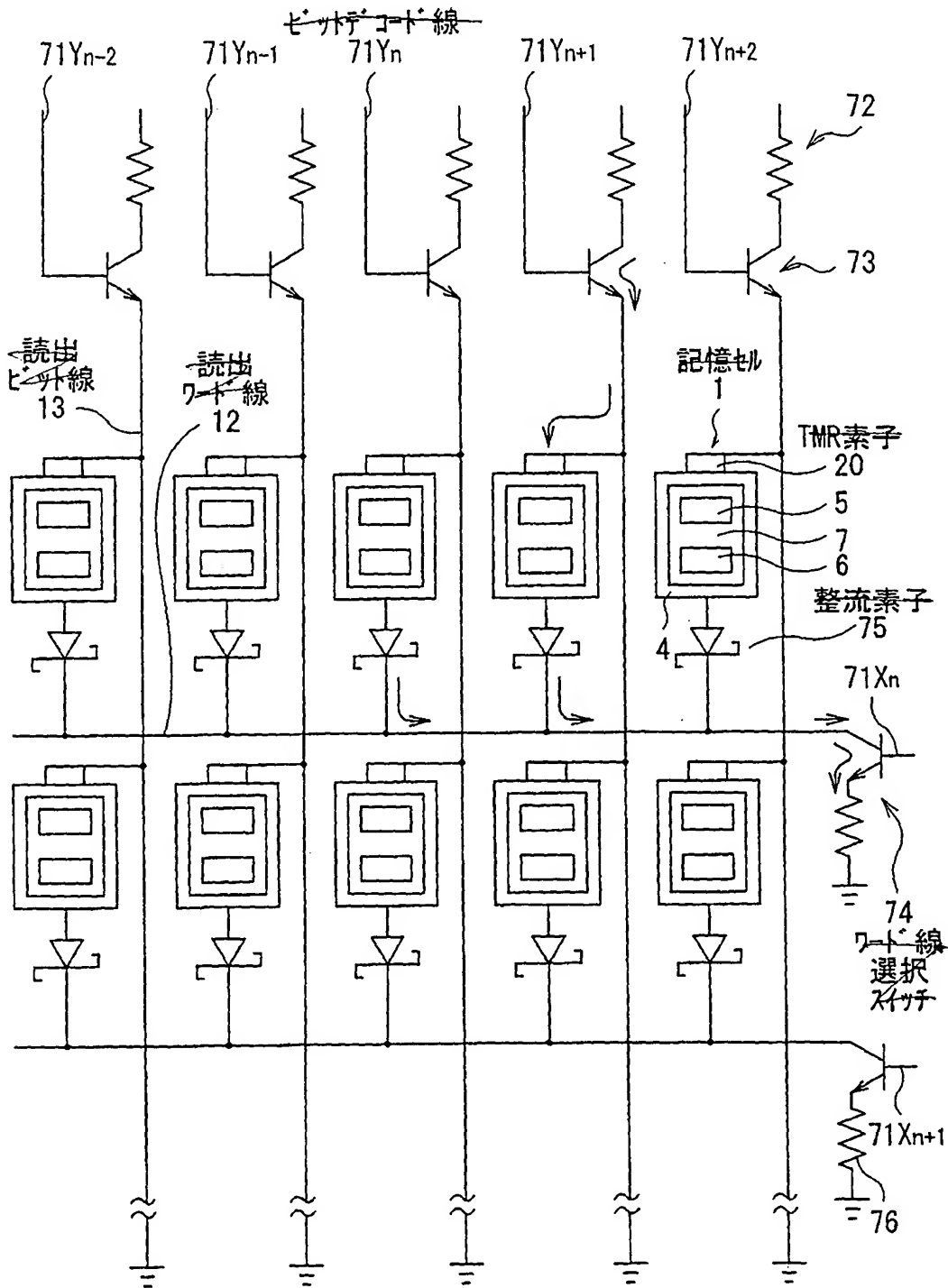


【図30】

Fig. 30



【図31】 Fig. 31



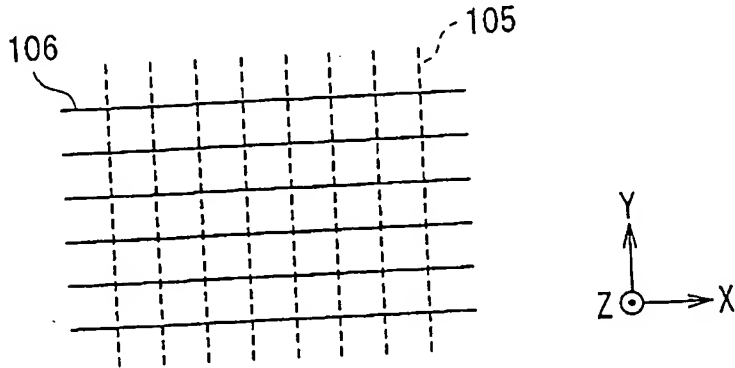
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[特許] 2002-307686

[受付日] 2002. 09. 13

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【図3-2】

Fig. 32



【図3-3】

Fig. 33

